

## Fixed 5V/1.5A Output Synchronous Boost in SOT23-5

### DESCRIPTION

The ETA1136 is a high efficiency synchronous step-up converter that can provide up to 1.5A to a fixed 5V output from a low voltage source. Unlike most step-up converter, it incorporates circuits that disconnect the input from output, during shutdown, short-circuit, output current overloading, or other events when output is higher than the input. This eliminates the need for an external MOSFET and its control circuitry to disconnect the input from output, and provides robust output overload protection.

A switching frequency of 1MHz minimizes solution footprint by allowing the use of tiny and low profile inductors and ceramic capacitors. An internal synchronous MOSFET provides highest efficiency and with a current mode control that is internally compensated, external parts count is reduced to minimal.

ETA1136 is housed in a tiny SOT23-5 package.

### FEATURES

- ◆ Output Disconnect
- ◆ Short-circuit Protection
- ◆ Fixed 5V/1.5A Output Power
- ◆ Output to Input Reversed Current Protection
- ◆ Up to 96% Efficiency
- ◆ 40 $\mu$ A No load  $I_Q$  and light load PFM Mode
- ◆ Internal Synchronous Rectifier
- ◆ Current Mode control
- ◆ Logic Control Shutdown and Thermal shutdown
- ◆ SOT23-5 Package

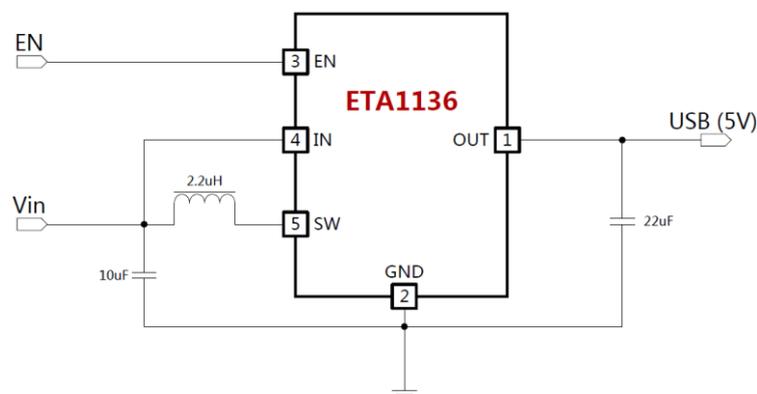
### APPLICATIONS

- ◆ USB OTG for MIDs, Smartphones
- ◆ Mobile back-up Battery Chargers
- ◆ Alkaline, NiCd, and NiMh batteries applications
- ◆ USB powered devices

### ORDERING INFORMATION

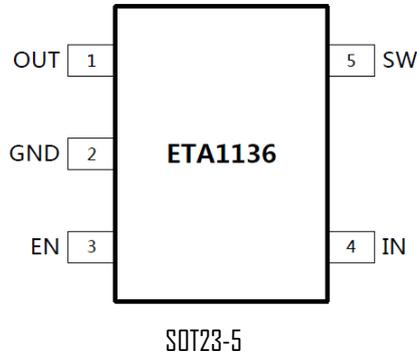
PART	PACKAGE PIN	TOP MARK
ETA1136S2F	SOT23-5	A3YW <i>YW=Date Code</i>

### TYPICAL APPLICATION



Typical Application Circuit

## PIN CONFIGURATION



## ABSOLUTE MAXIMUM RATINGS

(Note: Exceeding these limits may damage the device. Exposure to absolute maximum rating conditions for long periods may affect device reliability.)

SW Voltage.....	-0.3V (-5V for <10nS) to 5.5V (9V for <10nS)
All Other PIN Voltages.....	-0.3V to 5V
SW to ground current .....	Internally limited
Operating Temperature Range .....	-40°C to 85°C
Storage Temperature Range .....	-55°C to 150°C
Thermal Resistance	$\theta_{JA}$ $\theta_{JC}$
SOT23-5.....	180.....90..... °C/W
Lead Temperature (Soldering, 10sec) .....	260°C
ESD HBM (Human Body Mode) .....	2KV
ESD MM (Machine Mode) .....	200V

## ELECTRICAL CHARACTERISTICS

( $V_{IN} = 3.6V$ ,  $V_{OUT} = 5V$ , unless otherwise specified. Typical values are at  $T_A = 25^\circ C$ .)

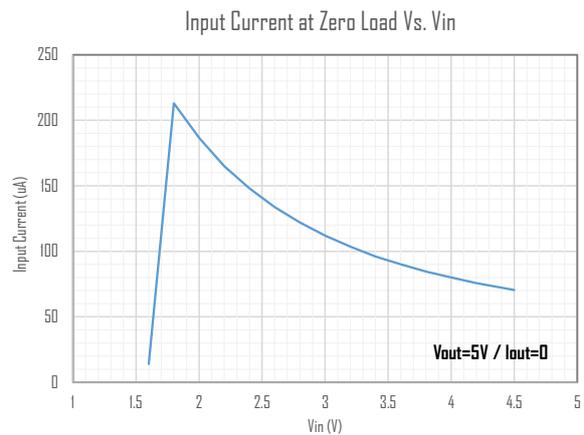
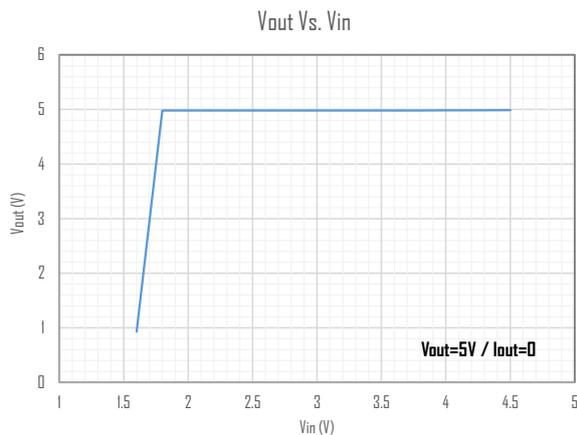
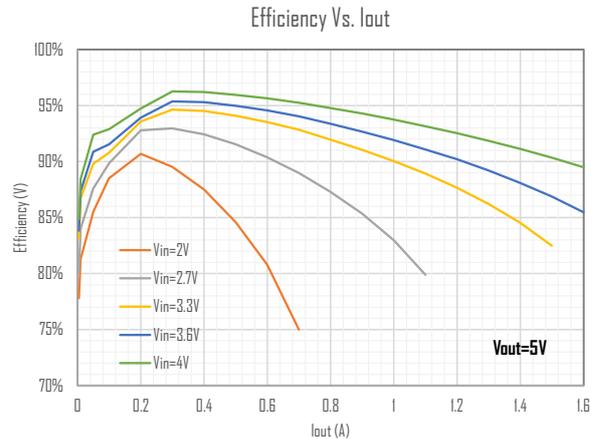
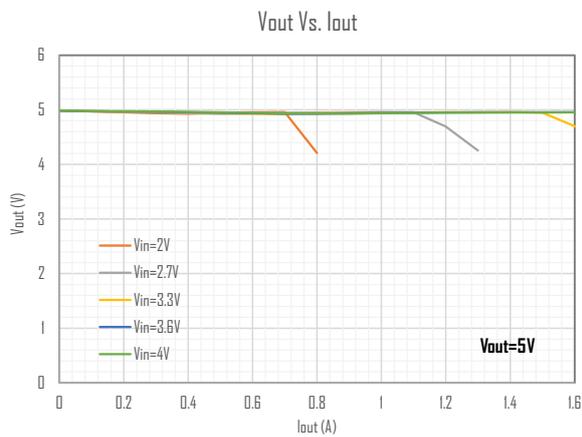
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Quiescent Current	$V_{EN}=IN$ , No load, Not switching		40	80	$\mu A$
Shutdown Supply Current at $V_{IN}$	$V_{EN} = GND$		0.5	5	$\mu A$
$V_{IN}$ UVLO at Rising			1.8		V
$V_{IN}$ UVLO at Falling			1.5		V
Output Voltage		4.97	5.07	5.17	V
Switching Frequency		0.7	1	1.3	MHz
NMOS Switch On Resistance	$I_{SW}=100mA$		150		$m\Omega$
PMOS Switch On Resistance	$I_{SW}=100mA$		120		$m\Omega$
SW Leakage Current	$V_{OUT}=5V, V_{SW}=0$ or $5V, V_{EN}=GND$			10	$\mu A$
NMOS Switch Current Limit		2.2	2.8		A
Start-up Current Limit			1.5		A
Short Circuit Hiccup time	ON		3.5		ms
	OFF		75		ms
EN Input Current		-1	0	1	$\mu A$
EN Input Low Voltage				0.6	V
EN Input High Voltage		1.5			V
Thermal Shutdown	Rising, Hysteresis= $25^\circ C$		160		$^\circ C$

## PIN DESCRIPTION

PIN #	NAME	DESCRIPTION
1	OUT	Output pin. Bypass with a 22 $\mu$ F or larger ceramic capacitor closely between this pin and GND
2	GND	Ground Pin
3	EN	Enable pin for the IC. Drive this pin high to enable the part, low to disable.
4	IN	Input Supply Voltage. Bypass with a 4.7 $\mu$ F ceramic capacitor to GND
5	SW	Inductor Connection. Connect an inductor Between SW and the input.

## TYPICAL CHARACTERISTICS

(Typical values are at  $T_A = 25^\circ\text{C}$  unless otherwise specified.)



## APPLICATION INFORMATION

### Loop Operation

The ETA1136 is a wide input range, high-efficiency, DC/DC step up switching regulator, integrated with a 120m $\Omega$  Low Side Main MOSFET and 150m $\Omega$  synchronous MOSFET. It uses a PWM current-mode control scheme. An error amplifier integrates error between the FB signal and the internal reference voltage. The output of the integrator is then compared to the sum of a current-sense signal and the slope compensation ramp. This operation generates a PWM signal that modulates the duty cycle of the power MOSFETs to achieve regulation for output voltage.

The peak current of the NMOS switch is also sensed to limit the maximum current flowing through the switch and the inductor. The typical peak current limit is set to 2.8A. An internal temperature sensor prevents the device from getting overheated in case of excessive power dissipation.

### Light Load Operation

Traditionally, a fixed constant frequency PWM DC/DC regulator always switches even when the output load is small. When energy is shuffling back and forth through the power MOSFETs, power is lost due to the finite RDSONs of the MOSFETs and parasitic capacitances. At light load, this loss is prominent and efficiency is therefore very low. ETA1136 employs a proprietary control scheme that improves efficiency in this situation by enabling the device into a power saving mode during light load, thereby extending the range of high efficiency operation.

### Short-Circuit Protection

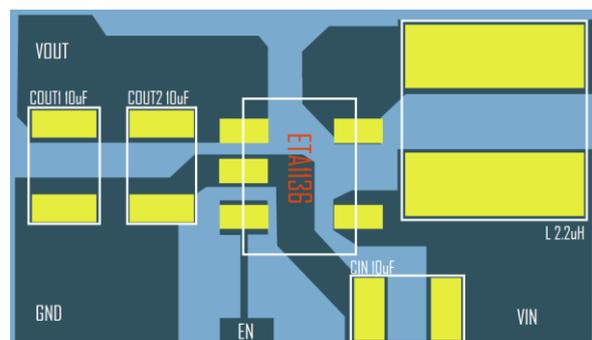
Unlike most step-up converters, the ETA1136 allows for short circuits on the output. In the event of a short circuit, the device first turns off the NMOS when the sensed current reaches the current limit. After  $V_{OUT}$  drops below  $V_{IN}$  the device then enters a linear charge period with the current limited same as with the start-up period. In addition, the thermal shutdown circuits disable switching if the die temperature rises above 150 $^{\circ}$ C.

### Down Mode ( $V_{IN} > V_{OUT}$ ) Operation

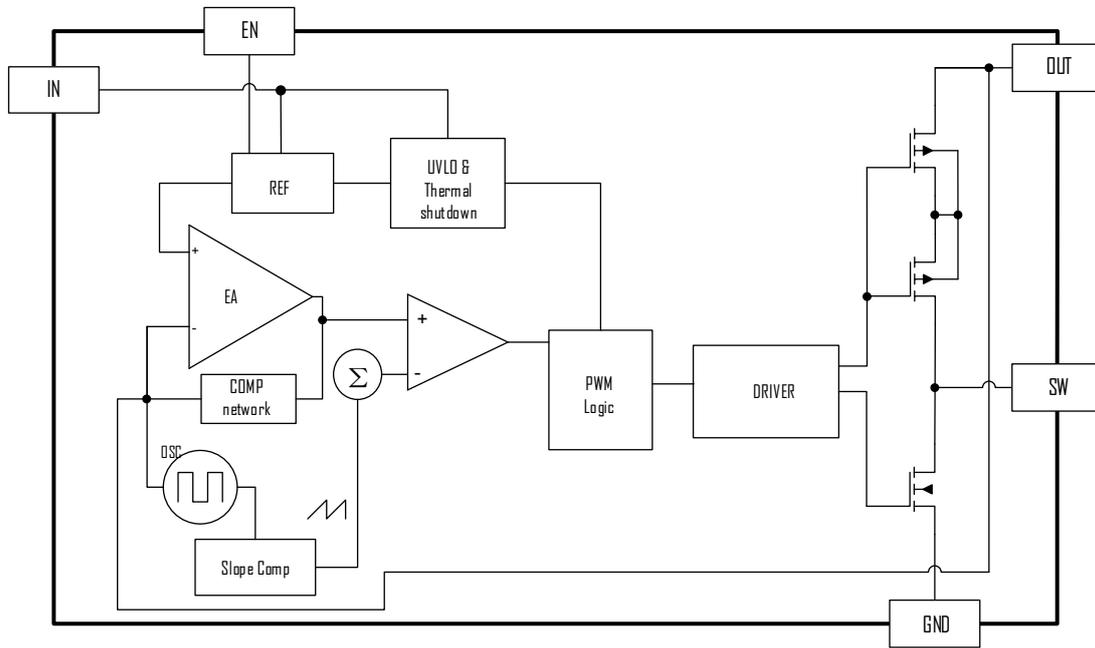
The ETA1136 will continue to supply the output voltage even when the input voltage exceeds the output voltage. Since the PMOS no longer acts as a low-impedance switch in this mode, power dissipation increases within the IC to cause a sharp drop in efficiency. Limit the maximum output current to maintain an acceptable junction temperature.

## PCB GUIDELINES

A recommended PCB layout is shown on the right hand. Please place the output capacitors (COUT1 and COUT2) just besides the chip. And wire GND out underneath the IC body. And thus, the performance of ETA1136 is guaranteed, and a very compact PCB design is also achieved



## BLOCK DIAGRAM



## PACKAGE OUTLINE

Package: SOT23-5

