

Ultra Low Power PMU with 40V Linear Charger and 5V Boost Converter

DESCRIPTION

ETA9698 is an ultra low power PMU with 2 channels: a 40V single cell Li⁺ battery linear charger and a 5V Boost synchronous converter with true-shutoff function. The linear charger fully integrated constant current (CC) / constant voltage (CV) control module and charge FET, with minimal external devices. It also has pre-charge function for trickle charging deeply discharged battery and its fast charge current can be programmed by an external resistor. When CV charge stage is entered, charge will be terminated once the charge current drops to 1/10 of the programmed value. A “STAT” pin is also available to indicate the charge status. And the low power 5V Boost converter is capable of delivering 0.4A current at 5V output. It can be shut down by pulling ENBST low, it will disconnect the output from the BAT to further decrease the system standby power. Its 1.4MHz switching frequency enable a very small external inductor with inductance as low as 2.2uH. The ETA9698, as a whole, is an ideal for the system solution that requires very low standby power and compact PCB board size.

ETA9698 is housed in a ESOP10 package

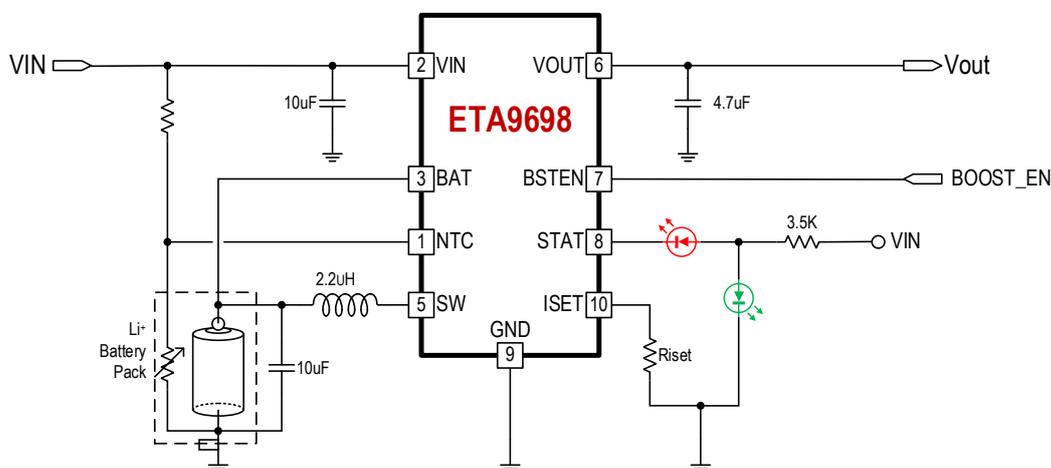
FEATURES

- ◆ Ultra low IQ when standby, I_{bat}<5uA
- ◆ 40V input standoff voltage
- ◆ 4.2V charge termination voltage
- ◆ Charge current programmable, up to 1.2A
- ◆ Output Disconnect and SCP at 5V output
- ◆ 5V/0.4A Output Power
- ◆ Up to 94% Efficiency for Boost converter
- ◆ Functioning with NO BATTERY
- ◆ Logic Control Shutdown
- ◆ Thermal shutdown
- ◆ ESOP10 Package

APPLICATIONS

- ◆ TWS BT earbuds charge case
- ◆ Bluetooth application
- ◆ Battery powered IOT module
- ◆ Power Bank

TYPICAL APPLICATION

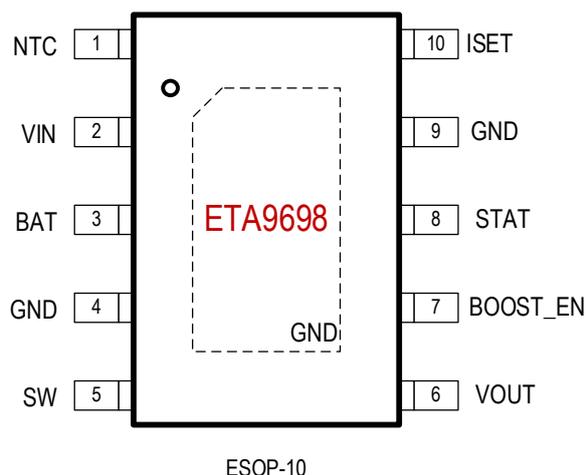


ORDERING

INFORMATION

PART No.	PACKAGE	TOP MARK	Pcs/Reel
ETA9698E10	ESOP10	ETA9698 YWW2L	4000

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS

(Note: Exceeding these limits may damage the device. Exposure to absolute maximum rating conditions for long periods may affect device reliability.)

VIN, BAT Voltage.....	-0.3V to 40V
SW, ISET, NTC Voltage.....	-0.3V to 6.5V
STAT, BSTEN, OUT Voltage.....	-0.3V to 6.5V
SW to ground current	Internally limited
Operating Temperature Range	-40°C to 85°C
Storage Temperature Range	-55°C to 150°C
Thermal Resistance θ_{JA} θ_{JC}	
ESOP10.....	50.....10.....°C/W
Lead Temperature (Soldering 10sec).....	260°C
ESD HBM (Human Body Mode)	2KV
ESD MM (Machine Mode)	200V

ELECTRICAL CHARACTERISTICS

($V_{BAT}=3.8V$, $V_{IN}=5V$, $V_{OUT} = 5V$, unless otherwise specified. Typical values are at $T_A = 25^\circ C$.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Input Standoff Voltage		40			V
Input Over-Voltage Protection Voltage	VIN rising, hys=0.4V	5.7	6.1	6.5	V
Input Voltage Range for Charging		4.25		6	V
Input Supply Current	Charge Mode		300	2000	μA
	Standby Mode (Charge Terminated)		100	200	μA
	Shutdown Mode (ISET Not Connected, nEN=VIN, VIN<VBAT, or VIN<VULO)		30	60	μA
Regulated Output (Float) Voltage	Rset = 10K, IBAT = 40mA	4.16	4.2	4.24	V
BAT Pin Current	Rset = 3.6K, Current Mode, VBAT=3.8V	465	515	565	mA
	Rset = 1.6K to 8K, Current Mode	90	100	110	%ICHRG
	Standby Mode, VBAT = 4.2V		2	3	μA
	Shutdown Mode, ISET Not Connected	0	0.4	0.7	μA
	Sleep Mode, VIN = 0V	0	0.4	0.7	μA

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Trickle Charge Current	VBAT < VTRIKL	4	12	20	%ICHRG
Trickle Charge Threshold Voltage	VBAT Rising	2.45	2.60	2.75	V
Trickle Charge Hysteresis Voltage			100		mV
VIN Under-voltage Lockout Threshold	From VIN Low to High	3.3	3.6	3.9	V
VIN Under-voltage Lockout Hysteresis		0.4	0.55	0.65	V
nEN Pull-Down Resistance			2		MΩ
nEN Threshold	nEN Pin Rising, Charger Disable	1.6			V
	nEN Pin Falling, Charger Enable			0.6	
VIN-VBAT Lockout Threshold Voltage	VIN from Low to High	70	150	230	mV
	VIN from High to Low	20	70	130	
Termination Current Threshold		13	20	27	%ICHRG
ISET Pin Voltage	Current Mode, VBAT=4V	0.9	1	1.1	V
STAT/STDBY Pin Weak Pull-Down Current	V_STAT = 5V		0.1		μA
STAT/STDBY Pin Output Low Voltage	I_STAT or I_STDBY= 2.5mA		0.7	1.4	V
Recharge BAT Threshold Voltage	VFLOAT - VRECHRG	100	200	300	mV
Junction Temperature in Constant Temperature Mode			110		°C
Power FET "ON" Resistance (Between VCC and BAT)			0.85		Ω
Soft-Start Time	IBAT = 0 to IBAT = 90% Full Current, CSS=0.1uF		80		ms
ISET Pin Pull-Up Current			1		μA
NTC Threshold, Cold	Charger Suspended		43		% VIN
NTC Threshold, Hot	Charger Suspended		6		% VIN
NTC Threshold Hysteresis			2		% VIN
NTC Disable Threshold	Tie NTC to GND		0.1		V
NTC Input Leakage			0	1	μA
Boost Converter					
Quiescent Current at BAT	V _{ENBST} =IN, Vbat=4V		2	5	μA
Shutdown Supply Current at ENBST	V _{ENBST} =GND		0.5		μA
VBAT Startup Voltage	I _{OUT} =1mA		0.8		V
BAT Operation Voltage	After Start-up	1.0		4.5	V
Output Voltage at 5V		4.85	5	5.15	V
Switching Frequency			1.4		MHz
NMOS Switch On Resistance	I _{SW} =100mA		220		mΩ
PMOS Switch On Resistance	I _{SW} =100mA		160		mΩ
SW Leakage Current	V _{OUT} =5.2V, V _{EN} =GND, V _{SW} =5.2V or V _{SW} = 0V			10	μA

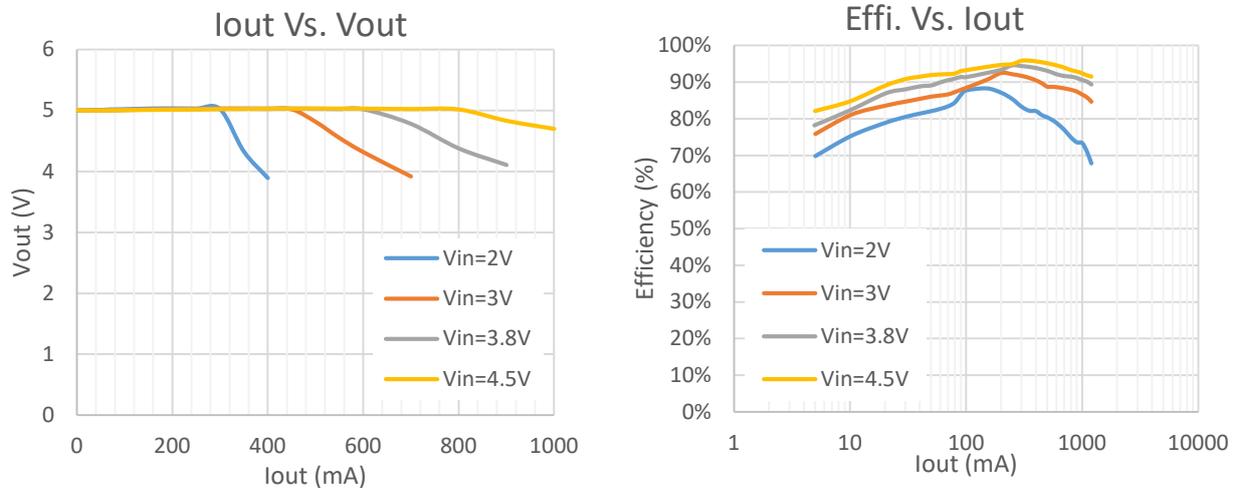
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
NMOS Switch Current Limit			1		A
Start-up Current Limit			1		A
Short Circuit Hiccup time	ON		1.3		ms
	OFF		33		ms
ENBST Input Current	$V_{EN}=5V$ or $0V$	-1	0	1	μA
ENBST High Voltage	$V_{OUT}=5V$	1.2			V
ENBST low Voltage	$V_{OUT}=5V$			0.4	V
Thermal Shutdown	Rising, Hysteresis= $25^{\circ}C$		160		$^{\circ}C$

PIN DESCRIPTION

PIN #	NAME	DESCRIPTION
1	NTC	Battery Temperature Monitoring input pin. It sets the valid temperature operating range for battery charging.
2	VIN	Input Supply Voltage. Bypass with a $10\mu F$ ceramic capacitor to GND
3	BAT	Connected to the battery positive terminal. Bypass with a $10\mu F$ ceramic capacitor to GND
4,9	GND	Ground
5	SW	Inductor Connection. Connect an inductor Between SW and the regulator output.
6	VOUT	Output pin. Bypass with a $4.7\mu F$ or larger ceramic capacitor closely between this pin and GND
7	BOOST_EN	Enable pin for the Boost converter. Drive this pin high to enable the part, low to disable.
8	STAT	Open-Drain Output for In Charging flag, The STAT pin outputs low when the battery is in charging. Upon the completion of the charge cycle, it becomes high-impedence.
10	ISET	Program, Monitor the charge current and Shutdown. This pin set to $1V$ in constant-current mode. The charge current is programmed by connecting a 1% resistor (Rset), between ISET to GND pin. The charge current can be calculated using the following formula: $I_{bat}=1850/R_{set}$ the ISET pin can also be used to switch the charger to shutdown mode by disconnecting the program resistor from ground.

TYPICAL CHARACTERISTICS

(Typical values are at $T_A = 25^\circ\text{C}$ unless otherwise specified.)



APPLICATION INFORMATION

ETA9698 is an ultra low power PMU with 2 channels: a 40V single cell Li^+ battery linear charger and a 5V Boost synchronous converter with true-shutoff function.

Normal Charge Cycle

The ETA9698 initiates a charge cycle once the voltage at the VIN pin rises above the UVLO threshold level. A 1% precision resistor needs to be connected from the ISET pin to ground. If the voltage at the BAT pin is less than 2.9V, the charger enters trickle charge mode. In this mode, the charge current is reduced to nearly 1/10 the programmed value until the battery voltage is raised to a safe level for full current charging.

The charger switches to constant-current mode as the BAT pin voltage rises above 2.9V, the charge current is thus resumed to full programmed value. When the final float voltage (4.2V) is reached, the ETA9698 enters constant-voltage mode and the charge current begins to decrease until it drops to 1/10 of the preset value and ends the charge cycle¹

Programming Charge Current

The charge current is programmable by setting the value of a precision resistor connected from the ISET pin to ground. The charge current is 1000 times of the current out of the ISET pin. The charge current out of the BAT pin can be determined at any time by monitoring the ISET pin voltage using the following equation:

$$I_{\text{bat}} = 1850 / R_{\text{iset}}$$

Charge Termination

The ETA9698 keeps monitoring the ISET pin during the charging process. It terminates the charge cycle when the charge current falls to 1/10 the programmed value after the final float voltage is reached. When the ISET pin voltage

falls below 100mV for longer than t_{TERM} (typically 1ms), charging is terminated. The charge current is latched off and the ETA9698 enters standby mode, where the input supply current drops to 200 μ A. (Note: C/10 termination is disabled in trickle charging and thermal limiting modes).

During charging, the transient response of the circuit can cause the ISET pin to fall below 100mV temporarily before the battery is fully charged, thus can cause a premature termination of the charge cycle. A 1ms filter time on the termination comparator can prevent this from happening. Once the average charge current drops below 1/10 the programmed value, the ETA9698 terminates the charge cycle and ceases to provide any current through the BAT pin. In this state, all loads on the BAT pin must be supplied by the battery.

The ETA9698 constantly monitors the BAT pin voltage in standby mode and resume another charge cycle if this voltage drops below the recharge threshold. User can also manually restart a charge cycle in standby mode either by removing and then reapplied the input voltage or restart the charger using the ISET pin.

Charge Status Indicator

There are 2 different states of the charge status, one is IN CHRGING, and the other is CHARGING FINISHED. STAT is the pin to pull low during IN CHARGING status and become high impedance in CHARGING FINISHED status.

High Temperature Fold-back

Build-in feedback circuitry mechanism can reduce the value of the programmed charge current once the die temperature tends to rise above 100°C, hence prevents the temperature from further increase and ensure device safe operation.

Under-voltage Lockout (UVLO)

Build-in under-voltage lockout circuit monitors the input voltage and keeps the charger in shutdown mode until VIN rises above the under-voltage lockout threshold. The UVLO circuit has a built-in hysteresis of 500mV. Furthermore, to protect against reverse current in the power MOSFET, the UVLO circuit keeps the charger in shutdown mode if VIN falls to within 30mV of the battery voltage. If the UVLO comparator is tripped, the charger will not come out of shutdown mode until VIN rises 100mV above the battery voltage.

Automatic Recharge

After the termination of the charge cycle, the ETA9698 constantly monitors the BAT pin voltage and starts a new charge cycle when the battery voltage falls below 4.08V, keeping the battery at fully charged condition. ISET pin output enters a strong pull-down state during recharge cycles.

Battery Temperature Monitoring

ETA9698 continuously monitors temperature by measuring the voltage of NTC pin. A negative or positive temperature coefficient thermistor and an external voltage divider typically develop this voltage. ETA9698 compares this voltage

against its internal 80%VIN and 45%VIN thresholds to determine if charging is allowed. The temperature sensing circuit is immune to any fluctuation in VIN, since both the external voltage divider and the internal thresholds 80%VIN and 45%VIN are referenced to VIN. If the NTC pin is connected to GND will disable the temperature-sensing feature.

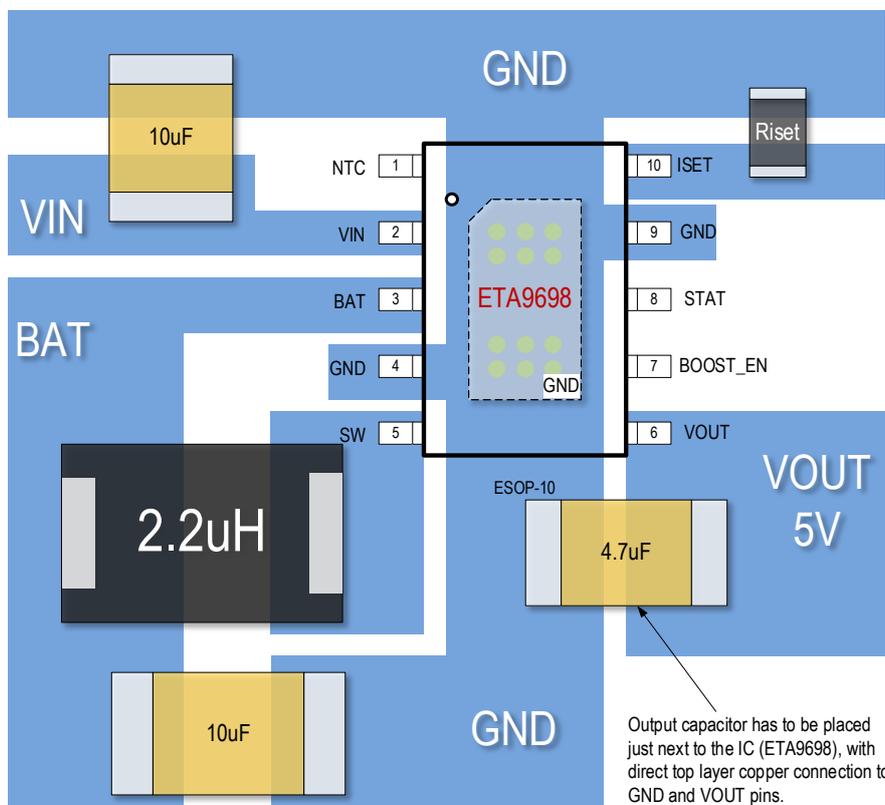
Ultra low current consumption at Light Load Boost Operation

Traditionally, a fixed constant frequency PWM DC/DC regulator always switches even when the output load is small. When energy is shuffling back and forth through the power MOSFETs, power is lost due to the finite RDSOns of the MOSFETs and parasitic capacitances. At light load, this loss is prominent and efficiency is therefore very low. ETA9698 employs a proprietary control scheme that improves efficiency in this situation by enabling the device into a power saving mode during light load and the no load quiescent current can be lower than 5µA.

Output (VOUT pin) Short-Circuit Protection

Unlike most step-up converters, the ETA9698 allows for short circuits on the output. In the event of a short circuit, the device first turns off the NMOS when the sensed current reaches the current limit. When OUT drops below IN, the device then enters a linear charge period with the current limited same as with the start-up period. In addition, the thermal shutdown circuits disable switching if the die temperature rises above 160°C.

PCB GUIDELINES



PACKAGE OUTLINE

Package: ESOP10

